

Ali Iranli

Objective	Full time job position
Education	<p>2002–Dec. 2005 University of Southern California Los Angeles, CA Ph.D. Electrical Engineering Concentrated on low power embedded systems emphasis on LCD displays and WLAN Advisor Dr. Massoud Pedram</p> <p>2000–2002 University of Southern California Los Angeles, CA M.S. VLSI Design Concentrated on low power digital circuit design emphasis on finite state machines Advisor Dr. Massoud Pedram</p> <p>1996-2000 Sharif University of Tech. Tehran B.S. Electrical Engineering Concentrated on general electrical engineering topics emphasis on circuit design</p>
Publications	<p>B-1. "System-Level Power Management: An Overview," A. Iranli and M. Pedram, In The VLSI Handbook, Edited by W-K. Chen, Taylor and Francis, 2006.</p> <p>J-1. "Cycle-based Decomposition of Markov Chains with Applications to Low Power Synthesis and Sequence Compaction for Finite State Machines" A. Iranli, M. Pedram, to appear in IEEE Transactions on Computer Aided Design.</p> <p>J-2. "A Game-Theoretic Approach to Low Energy Wireless Video Streaming" A. Iranli, H. Fatemi, M. Pedram, submitted to IEEE Transactions on Computer Aided Design, Second Revision.</p> <p>J-3. "HVS-Aware Dynamic Backlight Scaling in TFT LCD's", A. Iranli, M. Pedram, submitted to IEEE Transactions on Very Large Scale Integration (VLSI) Systems.</p> <p>C-1. "Crosstalk Analysis in nanometer technologies", S. Nazarian, A. Iranli, M. Pedram, to appear in Proc. of Great Lakes Symposium on VLSI (GLSVLSI), Apr. 2006.</p> <p>C-2. "Energy efficient strategies for deployment of a two-level wireless sensor networks" A. Iranli, M. Maleki, M. Pedram, Proc. of International Symposium on Low Power Electronics and Design, Jun. 2005.</p> <p>C-3. "Dynamic Tone Mapping for low power display systems" A. Iranli, M. Pedram, Proc. of Design Automation Conference, Jun. 2005.</p> <p>C-4. "Lifetime-aware intrusion detection under safeguarding constraints", A. Iranli, H. Fatemi, M. Pedram, Proc. Fourth International Conference on Information Processing in Sensor Networks, April 2005.</p> <p>C-5. "HEBS: Histogram Equalization for Backlight Scaling", A. Iranli, H. Fatemi, M. Pedram, Proc. of Design Automation and Test in Europe, Mar. 2005.</p> <p>C-6. "A Game Theoretic Approach to Low Energy Wireless Video Streaming", A. Iranli, K. Choi, M. Pedram, Proc. of Design Automation and Test in Europe, Mar. 2004.</p>

Experience (continued)	<p>1996–1998 Advanced Industrial Research Lab. Tehran, Iran</p> <p>Research Engineer</p> <p>Autonomous mobile robot feasibility study Setting up a simulation environment for AMR in MATLAB</p> <p>Fall 2002 University of Southern California Los Angeles, CA</p> <p>Teaching Assistant</p> <p>Computer Aided Design of Digital Systems I (EE680).</p> <p>Fall 2003 University of Southern California Los Angeles, CA</p> <p>Teaching Assistant</p> <p>Interconnection Networks (EE 659).</p> <p>Fall 2004 University of Southern California Los Angeles, CA</p> <p>Teaching Assistant</p> <p>CMOS VLSI Circuit Design (EE477).</p>
Current Research Interests	<p>Low power digital circuit and systems design</p> <p>Power aware CAD tools</p> <p>FPGA synthesis and technology mapping</p> <p>Distributed data Mining and processing Network</p> <p>Low power Display (LCDs/OLEDs) and Communication systems design</p> <p>Adaptive video and audio transceiver systems for low power</p> <p>Low power digital circuit and systems design</p> <p>Quantum and Molecular Computing</p>
Computer Skills	VHDL, Verilog, Cadence, HSpice, SIS, C++, GTK, Tcl, Linux, Windows
Professional service	<p>Reviewer for IEEE Transactions on Computer Aided Design (TCAD).</p> <p>Reviewer for ACM Transactions on Design Automation of Electronic Systems.</p> <p>Reviewer for different Computer Aided Design Community Conferences, e.g. ICCAD, ISLPED, DAC, ASDAC, etc.</p> <p>Assistant organizer of the Young Student Support Program (YSSP) in Design Automation Conference.</p>
Courses	<p>VLSI Circuit Design I, II (EE577a, b),</p> <p>ASIC Design (EE560),</p> <p>Logic Synthesis (EE681) & Physical design (EE680),</p> <p>Computer Arch. (EE557),</p> <p>Stochastic Calculus of Finance (MATH 503),</p> <p>Computational Molecular Biology (MATH 578a, b),</p> <p>Quantum Information Processing (EE599)</p>
References	<p>Massoud Pedram</p> <p>Professor, University of Southern California Department of EE-Systems, EEB-344, 3740 McClintock Ave., Los Angeles, CA 90089-2562, tel: (213) 740-4458</p> <p>Sandeep K. Gupta</p> <p>Professor, University of Southern California Department of EE-Systems, EEB-336, 3740 McClintock Ave., Los Angeles, CA 90089-2562, tel: (213) 740-2251</p>

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